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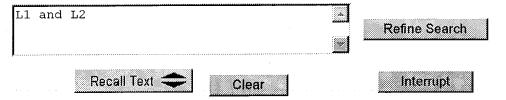
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Najjar, W.A.; Bohm, W.; Draper, B.A.; Hammes, J.; Rinker, R.; Beveridge, J.R Chawathe, M.; Ross, C.;

Computer, Volume: 36, Issue: 8, Aug. 2003

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> 19 A scheduling algorithm for synthesis of bus-partitioned architectur Moshnyaga, V.G.; Ohbayashi, F.; Tamaru, K.;

Design Automation Conference, 1995. Proceedings of the ASP-DAC '95/CHDL '95/VLSI '95., IFIP International Conference on Hardware Description Languag IFIP International Conference on Very Large Scale Integration., Asian and Sou Pacific, 29 Aug.-1 Sept. 1995

Pages:43 - 48

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20 Formal description of data conversion algorithms for automatic synthesis

Horta, N.C.; Franca, J.E.;

Circuits and Systems, 1994., Proceedings of the 37th Midwest Symposium

on , Volume: 1 , 3-5 Aug. 1994

Pages: 370 - 373 vol.1

[Abstract] [PDF Full-Text (384 KB)]

21 A synthesis approach for the generation of parallel architectures

Kroumba, S.M.; Bois, G.; Savaria, Y.;

Circuits and Systems, 1994., Proceedings of the 37th Midwest Symposium

on , Volume: 1 , 3-5 Aug. 1994

Pages:323 - 326 vol.1

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22 Generating the optimal graph representations from the instruction tables of circuits

Chen, C.Y.R.; Tseng, W.-C.;

Custom Integrated Circuits Conference, 1994., Proceedings of the IEEE 1994

May 1994

Pages:241 - 244

[Abstract] [PDF Full-Text (384 KB)] **IEEE CNF**

23 Proceedings Seventh Annual IEEE International ASIC Conference a **Exhibit**

ASIC Conference and Exhibit, 1994. Proceedings., Seventh Annual IEEE International, 19-23 Sept. 1994

[Abstract] [PDF Full-Text (96 KB)] IEEE CNF

24 Supporting system maintenance with automatic decomposition sch

Gopal, R.; Prasad, R.;

System Sciences, 1992. Proceedings of the Twenty-Fifth Hawaii International

Conference on , Volume: ii , 7-10 Jan. 1992

Pages:507 - 516 vol.2

[Abstract] [PDF Full-Text (948 KB)] **IEEE CNF**

25 Automated synthesis of asynchronous pipelines

Yau-Hwang Kuo; Shaw-Pyng Lo;

Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE Internationa

Symposium on , Volume: 2 , 3-6 May 1992

Pages:685 - 688 vol.2

[Abstract] [PDF Full-Text (328 KB)]

26 MxSICO: a silicon compiler for mixed analog digital circuits Berkcan, E.;

Computer Design: VLSI in Computers and Processors, 1990. ICCD '90. Proceedings., 1990 IEEE International Conference on , 17-19 Sept. 1990 Pages:33 - 36

[Abstract] [PDF Full-Text (424 KB)] IEEE CNF

27 MxSICO: a mixed analog digital compiler: application to oversampl A/D converters

Berkcan, E.;

Custom Integrated Circuits Conference, 1990., Proceedings of the IEEE 1990 16 May 1990

Pages:14.9/1

[Abstract] [PDF Full-Text (96 KB)] IEEE CNF

28 Singular value computations on a massively parallel machine Ewerbring, L.M.; Luk, F.T.;

TENCON '89. Fourth IEEE Region 10 International Conference , 22-24 Nov. 19 Pages: 348 - 351

[Abstract] [PDF Full-Text (336 KB)] IEEE CNF

29 MOPS-a data structure management system for the LEP/SPS contro system

Herr, W.;

Particle Accelerator Conference, 1989. 'Accelerator Science and Technology'., Proceedings of the 1989 IEEE , 20-23 March 1989 Pages:1713 - 1715 vol.3

[Abstract] [PDF Full-Text (252 KB)] IEEE CNF

30 Hermod: an interactive behavioral synthesizer for VLSI

Odani, M.; Hwang, S.Y.; Blank, T.;

Circuits and Systems, 1989., IEEE International Symposium on , 8-11 May 19 Pages:1871 - 1874 vol.3

[Abstract] [PDF Full-Text (296 KB)] IEEE CNF

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Compcon Spring '91. Digest of Papers , 25 Feb.-1 March 1991

Pages: 105 - 109

[Abstract] [PDF Full-Text (292 KB)]

2 Chip design of mel frequency cepstral coefficients for speech recogn Jia-Ching Wang; Jhing-Fa Wang; Yu-Sheng Weng; Acoustics, Speech, and Signal Processing, 2000. ICASSP '00. Proceedings. 20 IEEE International Conference on , Volume: 6 , 5-9 June 2000 Pages:3658 - 3661 vol.6

[PDF Full-Text (<u>388 KB)</u>] [Abstract] **IEEE CNF**

3 Unified functional decomposition via encoding for FPGA technology mapping

Jie-Hong Jiang; Jing-Yang Jou; Juinn-Dar Huang;

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume:

9 , Issue: 2 , April 2001

Pages:251 - 260

[PDF Full-Text (220 KB)] [Abstract]

4 A new partitioning method for LUT-based FPGAS

Seok-Bum Ko;

Electrical and Computer Engineering, 2003. IEEE CCECE 2003. Canadian Conference on , Volume: 1 , 4-7 May 2003

Pages: 103 - 106 vol.1

[Abstract] [PDF Full-Text (327 KB)] IEEE CNF

5 An implicit algorithm for support minimization during functional decomposition

Legl, C.; Wurth, B.; Eckl, K.;

European Design and Test Conference, 1996. ED&TC 96. Proceedings , 11-14 March 1996

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